IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Amplication Number	10/800938	Docket Number	CNTD 2072		
Application Number	10/600936	Docket Number	CN1R.2072		
Filed	03/15/04	Group Art Unit	2131		
Examiner		Customer No.	23669		
Application Title	MICROPROCESSOR APPARATUS AND METHOD FOR EMPLOYING CONFIGURABLE BLOCK CIPHER CRYPTOGRAPHIC ALGORITHMS				
First Named Inventor	G. GLENN HENRY				

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Fax Number:

571-273-8300

From:

HUFFMAN PATENT GROUP, LLC

Fax Number:

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135 (including this cover sheet)

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Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted, HUFFMAN PATENT GROUP, LLC					
	/Richard K. Huffman/				
By:					
	RICHARD K. HUFFMAN, P.E. Registration No. 41,082 Tel: (719) 575-9998				
	06/04/2006				

Date:

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INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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	06/04/2006							
Date: _								

PTO/SB/08A (08-03)
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Substitute for form 1449A/PTO				Application Number	10/800938	
INFORM	ATION DISC	LOS	JRE	Filing Date	03/15/04	
STATEMENT BY APPLICANT				First Named Inventor	G. Glenn Henry	
				Group Art Unit	2131	
(use as	many sheets as no	ecessary	'	Examiner Name		
Sheet	1	of	2	Attorney Docket Number	CNTR.2072	

			U.S. PATENT	DOCUMENTS	
Examiner Cite		Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures
Initials*	No.		Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
		2002191784	12-19-2002	Yup et al.	
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	FOREIGN PATENT DOCUMENTS									
Examiner	ner Cite Foreign Patent Document		Publication Date	Name of Patentee or	Pages, Columns, Lines,	76				
Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ^{5 (if known)}	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	<u>'</u>				
	AA	EP1271839	01-02-2003	Okada et al.						
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Signature		Considered	

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Substitute for form 1449A/PTO				Application Number	10/800938	
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STATEMENT BY APPLICANT				First Named Inventor	G. Glenn Henry	
				Group Art Unit	2131	
(use as	many sheets as n	ecessary	1	Examiner Name		
Sheet	2	of	2	Attorney Docket Number	CNTR.2072	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Τ2
	ВА	WU et al. "CryptoManiac: a fast flexible architecture for secure communication." Proceedings of the 28th. International Symposium on Computer Architecture. ISCA 2001. 06-30-2001 pages 104-113, XP010553867	
	ВВ	GLADMAN, BRIAN. "A Specification for Rijndael, the AES Algorithm." Online. 03/13/2003. XP002376287	
	ВС	BACKHUS E. "AES in FPGAS Implementierung Des Advanced Encryption Standards in Hardware." Elektronik, WEKA Fachzeitschriftenverlag, Poing, DE. Vol. 52, no.8. 04/15/2003 pages 54-56, 58, 60. XP001160048	
	BD	DAEMON J et al. "AES Proposal: RIJNDAEL." AES Proposal 09/03/1999. pages 1-45, XP001060386	
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